# SPI2TPL Evaluation Board for BMA6X02 with SPI interface and TPL Communication

## KIT6X02AS2T1 NEW
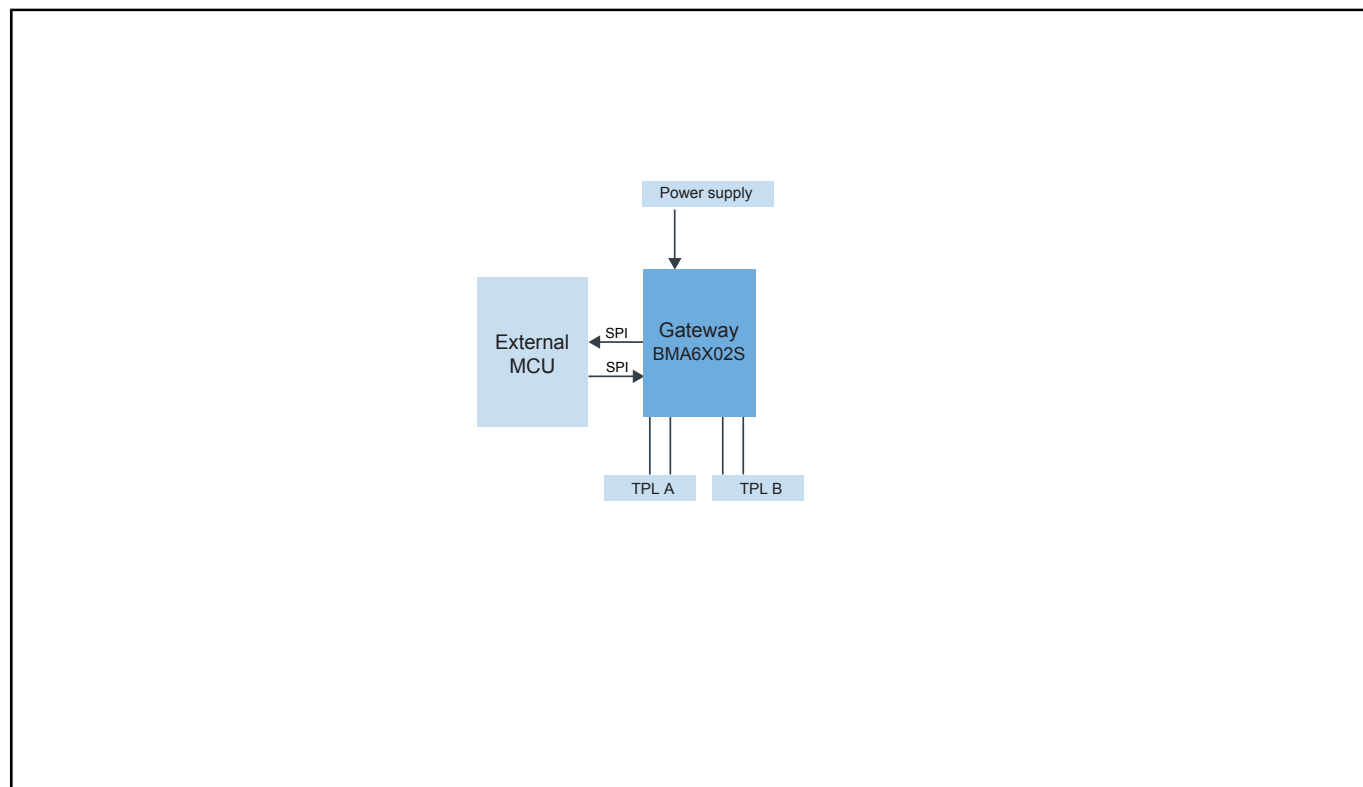
**Preproduction**

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The KIT6X02AS2T1 serves as a hardware evaluation tool in support of the BMA6X02. The BMA6X02 is a gateway router that can route TPL messages from the microcontroller (MCU) to two different TPL ports. It is designed for use in both automotive and industrial applications. The KIT6X02AS2T1 is an ideal board for rapid prototyping of the BMA6X02 for SPI interfaces, hence its nickname SPI2TPL.

# KIT6X02AS2T1 Block Diagram



View additional information for SPI2TPL Evaluation Board for BMA6X02 with SPI interface and TPL Communication.

Note: The information on this document is subject to change without notice.